

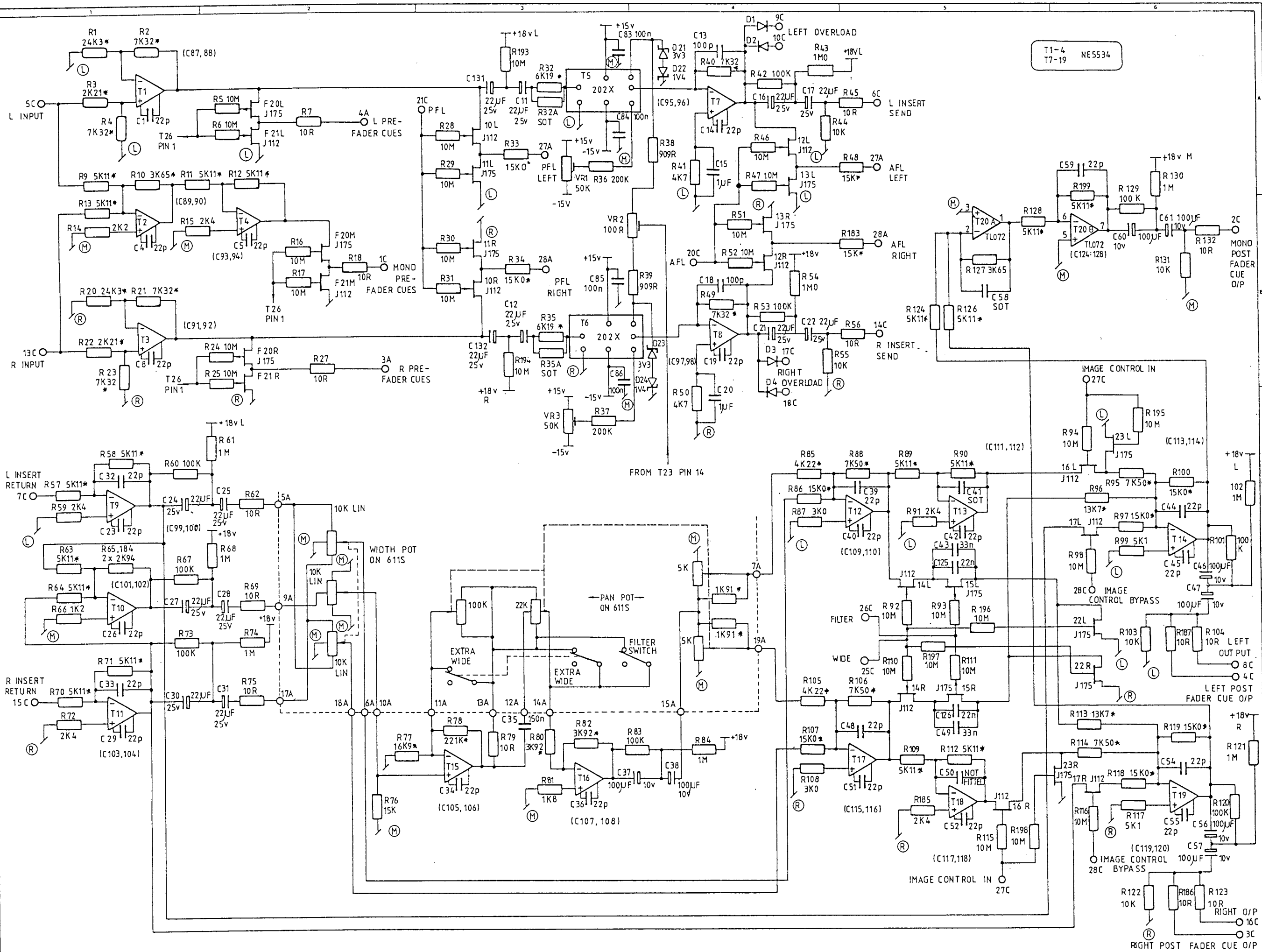
64 Way Eurocard Connector List

From:

To: 82E213 Stereo VCA and Image Control

No Row A	No Row C
1 Spare	1 Mono pre fader cues output
2 Spare	2 Mono post fader cues output
3 R pre fader cues output	3 R post fader cues output
4 L pre fader cues output	4 L post fader cues output
5 L output to image control	5 L input
6 Image control wiper L	6 L insert send to switchcard
7 L pan control wiper	7 L insert return from switchcard
8 OV (L)	8 L output
9 Mono output to Image control	9 L Overload output +
10 Image control wiper mono	10 L Overload output -
11 Wide makeup gain control	11 Spare
12 Filter compensation	12 OV (L)
13 Mono buffer output	13 R input
14 Mono Pan buffer input	14 R insert send to switchcard
15 Mono Pan buffer output	15 R insert return from switchcard
16 OV (R)	16 R output
17 R output to image control	17 R Overload output +
18 R image control wiper	18 R Overload output -
19 R Pan control wiper	19 Cues cut FET
20 OV (R)	20 AFL FET
21 +18V	21 PFL FET
22 +18V	22 Solo mute bus
23 Spare	23 Cut switch
24 OV (M)	24 Dynamics enable FET
25 -18V	25 Wide FET
26 -18V	26 Filter FET
27 AFL/PFL L output	27 Image control in FET
28 AFL/PFL R output	28 Image control bypass FET
29 VCA to meter output	29 Dynamics side chain
30 Fader wiper	30 +5V to fader
31 Cut output	31 VCA Thumbwheel
32 Computer return	32 Computer send

T82213.50



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Rev.	Issue	Chkd	Details
P	a	7-84	PROTOTYPE
1	a	8-84	PRE FADER CUES CUT ADDED
2	a	8-84	PRE FADER CUES CUT DRIVER ADDED
3	a	9-84	FADER START COMPARATOR ADDED
3	b	1-85	POST FADER MONO CUES AMENDED. SHORTING FETS ON IMAGE CONTROL ADDED.
4	a	1-85	REDRAWN V.B.
5	a	3-85	R65, 77, 85, 105, 184 CHANGED
6	a	4-85	R153, 155 CHANGED
7	a	4-85	FADER REJECTION IMPROVED 4-85
8	a	7-85	C65, 64 CHANGED TO 220p
9	a	JH Oct85	VALUES CHANGED R32, 35.
10	a	DM Jan86	ASSY REV No AMENDED
10	b	4/86	10K LINS GANGED, EXTRA WIDE SWITCHES GANGED.
10	c	DTCA Oct86	POST FADE & MONO CUE ADDED GANGING ADDED TO PAN POT.
10	d	BC Nov86	ECON. NAMES ADDED. 2x3K32 RESISTORS NOW 1K91
10	e	DG Jan87	ECON NAMES AMENDED
11	a	BC Sept87	2x3V3 & 2x1V5 ADDED TO 202X. ECO274
11	b	18 JULY 89	DIODE REFERENCES D21-24 ADDED STEFF
12	a	18 JULY 89	ECO 4K/426 TH R32-35 WERE 3K32 NOW 6K19. R40-49 WERE 4K53 NOW 7K32 STEFF
12	b	22 AUG 89	R132 NOW R131 BC J112 ADDED STEFF
13	a	26 Sept 89	ECO 4K/469 BOARD RELAID STEFF
14	a	17 Apr 90	ECO 4K/482 NO CHANGE BC STEFF
15	a	BC 9-90	ECO 4K/599 B.O.M AMENDED STEFF

SHEET 10F 2

Title
STEREO VCA AND IMAGE CONTROL (AUDIO)

Drg. No. T82213-70

Solid State Logic
2-24

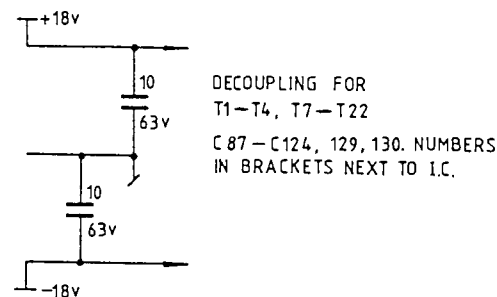
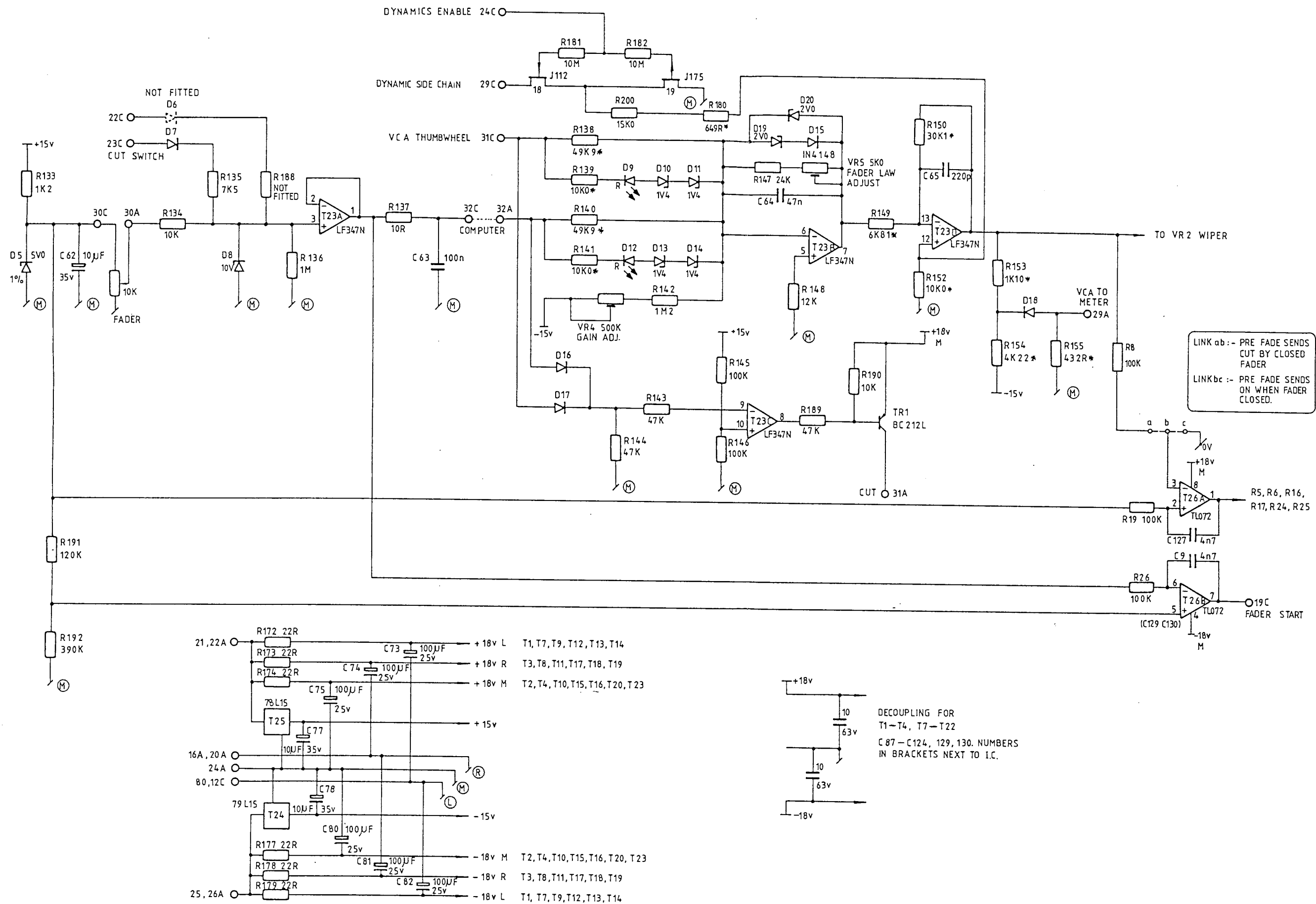
Rev.	Issue	Chkd	Details
P	a	7-84	PROTOTYPE
1	a	8-84	D19, D20, ADDED OVERLOAD DETECTOR REMOVED
2	a	6-84	CUES CUT DRIVE ADDED
3	a	9-84	FADER START COMPARATOR ADDED
3	b	1-85	POST FADER MONO CUES AMENDED. SHORTING FETS ON IMAGE CONTROL ADDED
4	a	1-85	REDRAWN V.B.
5	a	3-85	R65,77,85,105,184 CHANGED
6	a	4-85	R153,155 CHANGED
7	a	4-85	FADER REJECTION IMPROVED 4-85
8	a	7-85	C 65,64 CHANGED TO 220p
8	b	7-85	ORG. N° WAS T82213-70 SHT. 2
8	c	8-85	DYNAMICS VOLTAGE NOW FED TO T12, p12
8	d	8-85	C64 VALUE CHANGED
9	a	JH Oct85	VALUES CHANGED R180,147
9	b	DM Jan86	R147 VALUE AMENDED
10	a	DM Feb86	LINKS ADDED NEAR R8 ECO 108
10	b	BC Nov86	ECON. NAMES ADDED
10	c	DG Jan87	ECON 29C NAME AMENDED
11	a	Oct87	REVISION LEVEL UPDATED. ECO 276.
11	b	18 JULY 89	R188 NOT FITTED ADDED D9:D12 NOW LEDs.D10-11 D13-14 1V4 ADDED STEFF
12	a	18 JULY 89	ECO 4K/426 NO CHANGE STEFF
12	b	22 AUG 89	R180 NOW R200. CA324 ADDED TO T23. TL072 ADDED TO T26 STEFF
13	a	26 Sept 89	ECO 4K/469 BOARD RELAID STEFF
14	a	17 Apr 90	ECO 4K/482 T23 WAS CA324 NOW LF347N STEFF
15	a	BC 9-90	ECO 4K/599 B.O.M AMENDED STEFF

SHEET 2 OF 2

Title
STEREO VCA & IMAGE CONTROL

Org.No.
T82213-71

Solid State Logic
2-25



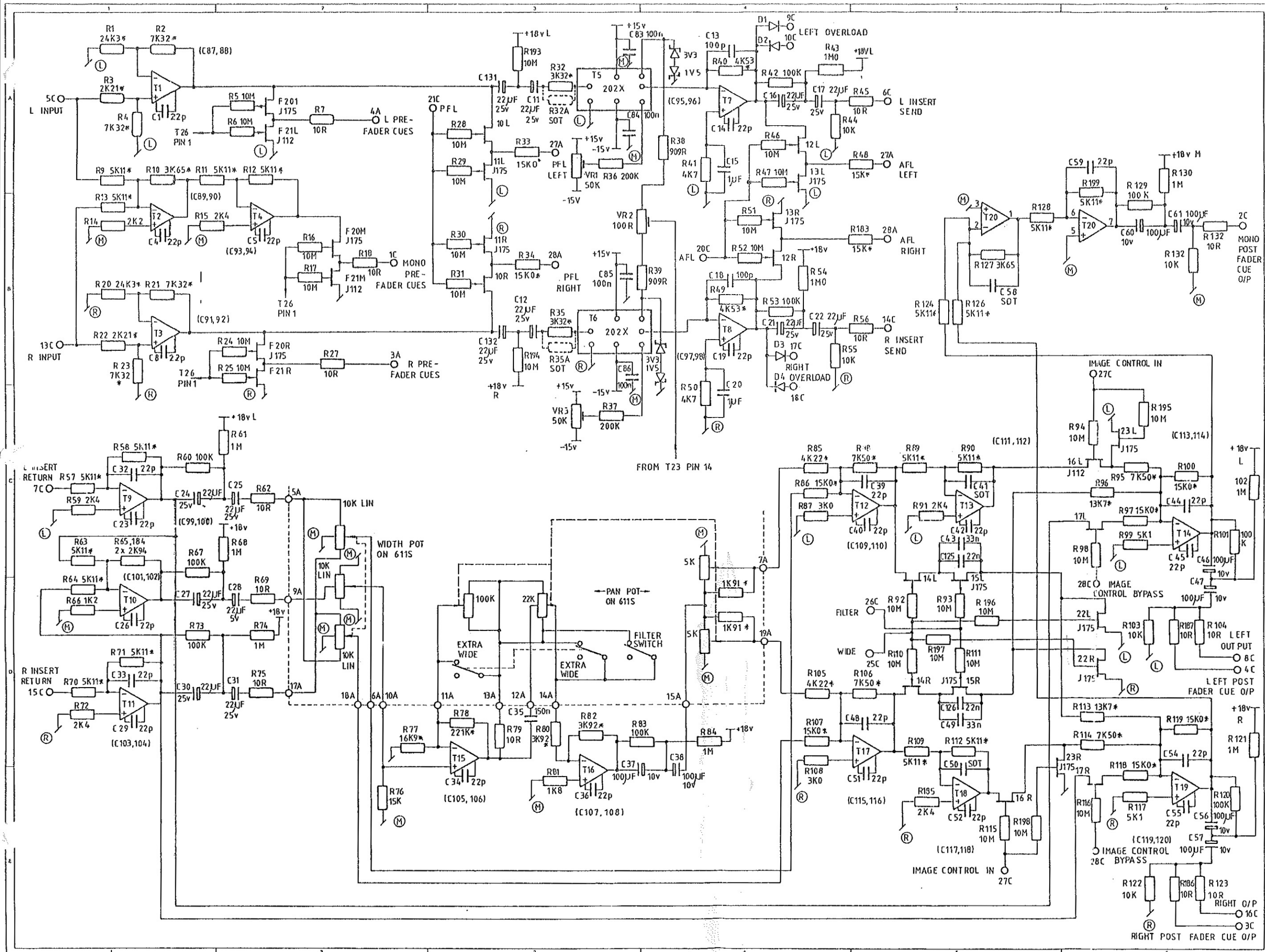
PIN N^os reversed! 1-32 = 32-1
on connector

64 Way Eurocard Connector List			
From:			
To: 82E213 Stereo VCA and Image Control			
No	Row A	No	Row C
1	Spare	1	Mono pre fader cues output
2	Spare	2	Mono post fader cues output
3	R pre fader cues output	3	R post fader cues output
4	L pre fader cues output	4	L post fader cues output
5	L output to image control	5	L input
6	Image control wiper L	6	L insert send to switchcard
7	L pan control wiper	7	L insert return from switchcard
8	OV (L)	8	L output
9	Mono output to Image control	9	L Overload output +
10	Image control wiper mono	10	L Overload output -
11	Wide makeup gain control	11	Spare
12	Filter compensation	12	OV (L)
13	Mono buffer output	13	R input
14	Mono Pan buffer input	14	R insert send to switchcard
15	Mono Pan buffer output	15	R insert return from switchcard
16	OV (R)	16	R output
17	R output to image control	17	R Overload output +
18	R image control wiper	18	R Overload output -
19	R Pan control wiper	19	Cues cut FET
20	OV (R)	20	AFL FET
21	+18V	21	PFL FET
22	+18V	22	Solo mute bus
23	Spare	23	Cut switch
24	OV (M)	24	Dynamics enable FET
25	-18V	25	Wide FET
26	-18V	26	Filter FET
27	AFL/PFL L output	27	Image control in FET
28	AFL/PFL R output	28	Image control bypass FET
29	VCA to meter output	29	Dynamics side chain
30	Fader wiper	30	+5V to fader
31	Cut output	31	VCA Thumbwheel
32	Computer return	32	Computer send

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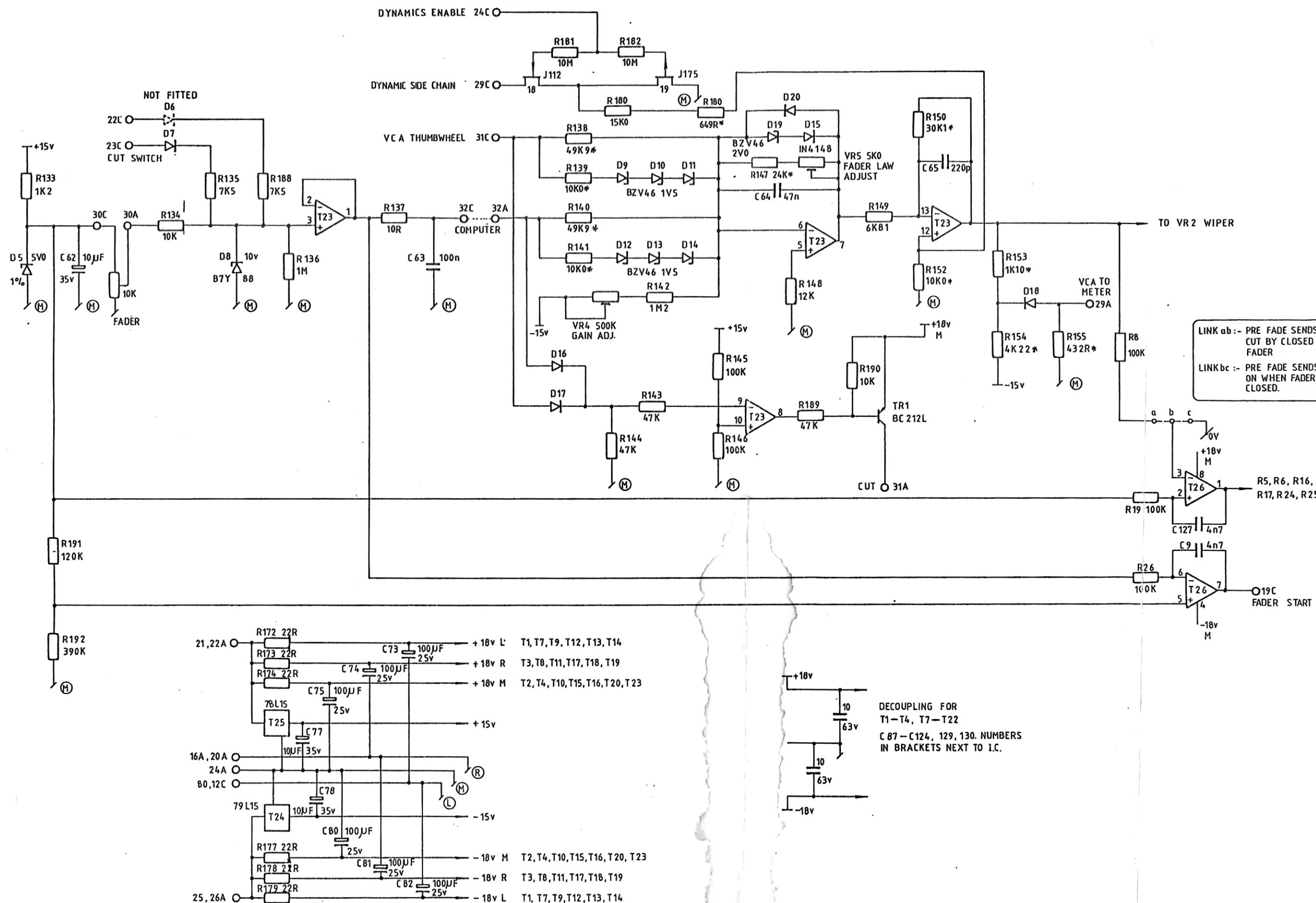
Rev.	Issue	Chkd	Details
P	a	784	PROTOTYPE
1	a	884	PRE FADER CUES CUT ADDED
2	a	884	PRE FADER CUES CUT DRIVER ADDED
3	a	984	FADER START COMPARATOR ADDED
3	b	185	POST FADER MONO CUES AMENDED. SHORT- ING FETS ON IMAGE CONTROL ADDED.
4	a	185	REDRAWN V.B.
5	a	385	R65, 77, 85, 105, 184 CHANGED
6	a	485	R153, 155 CHANGED
7	a	485	FADER REJECTION IMPROVED 4-85
8	a	785	C65, 64 CHANGED TO 220p
9	a	JH Oct85	VALUES CHANGED R32, 35.
10	a	DM Jan86	ASSY REV No AMENDED
10	b	4/86	10K LINS GANGED, EXTRA WIDE SWITCHES GANGED.
10	c	DTG Oct86	POST FADE & MONO CUE ADDED. GANGING ADDED TO PAN POT.
10	d	BC Nov86	ECON. NAMES ADDED. 2x3K32 RESISTORS NOW 1K91
10	e	DG Jan87	ECON. NAMES AMENDED
11	a	BC Sept87	2x3V3 & 2x1V5 ADDED TO 202X ECO276

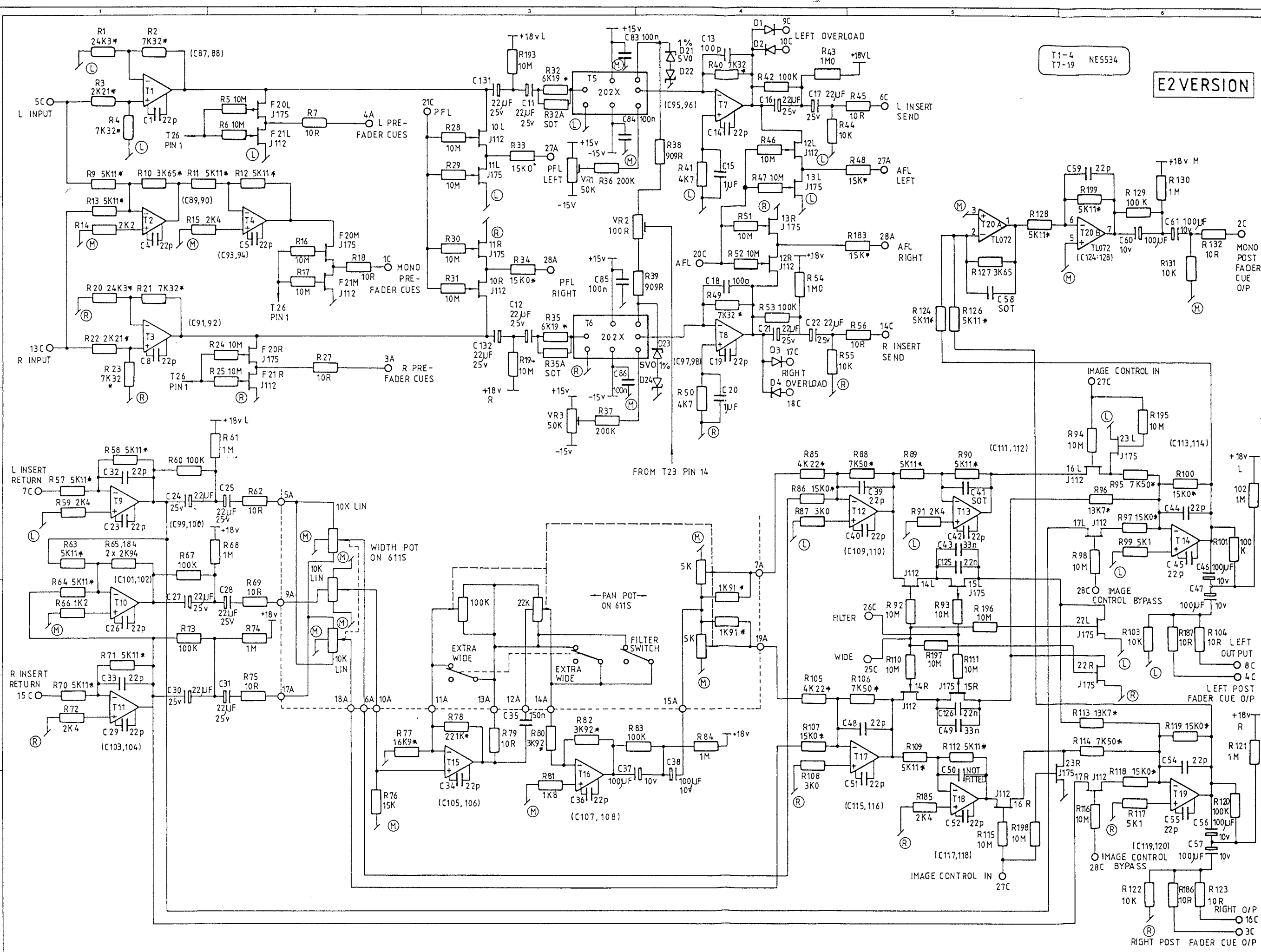


Title
STEREO VCA AND IMAGE
CONTROL (AUDIO)
Drg. No.
T82213-70
Solid State Logic

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Rev.	Issue	Chkd	Details
P	a	7-84	PROTOTYPE
1	a	8-84	D19, D20, ADDED OVERLOAD DETECTOR REMOVED
2	a	6-84	CUES CUT DRIVE ADDED
3	a	9-84	FADER START COMPARATOR ADDED
3	b	1-85	POST FADER MONO CUES AMENDED. SHORTING FETS ON IMAGE CONTROL ADDED
4	a	1-85	REDRAWN YB.
5	a	3-85	R65,77,85,105,184 CHANGED
6	a	4-85	R153,155 CHANGED
7	a	4-85	FADER REJECTION IMPROVED 4-85
8	a	7-85	C 65,64 CHANGED TO 220p
8	b	7-85	ORG. N° WAS T82213-70 SHT. 2
8	c	8-85	DYNAMICS VOLTAGE NOW FED TO T12,p12
8	d	8-85	C64 VALUE CHANGED
9	a	JH Oct85	VALUES CHANGED R180,147
9	b	DM Jan86	R147 VALUE AMENDED
10	a	DM Feb86	LINKS ADDED NEAR R8 ECO 108
10	b	BC Nov86	ECON. NAMES ADDED
10	c	DG Jan87	ECON 29C NAME AMENDED





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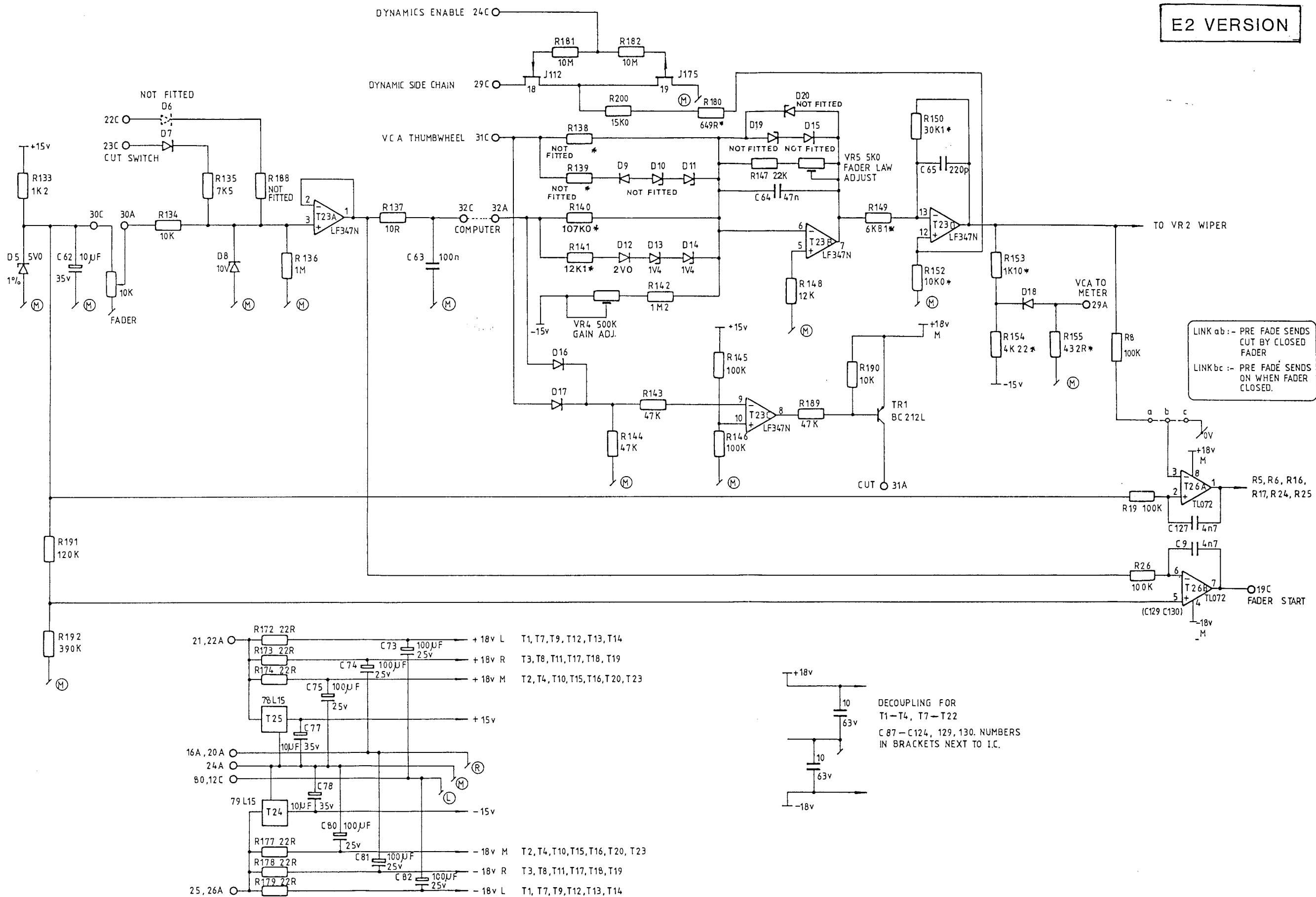
Rev.	Issue	Chkd	Details
0	A	MM NOV 90	NEW DRAWING

STEREO VCA AND IMAGE
CONTROL (AUDIO)

DRG. No. T82213-70-2

SHEET 1 OF 2

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Rev.	Issue	Chkd	Details
0	A	MM NOV 90	NEW DRAWING

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Title
STEREO VCA & IMAGE CONTROL

Drg. No.
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